

ABSTRACT:

An Erasable and programmable non-volatile cell, comprising

- a first transistor having a source, a drain and a gate;
- a floating capacitor having a floating gate and a control gate, said floating gate being connected to said gate of said first transistor; and
- 5 - means to detect the state, whether erased or programmed, of the cell;

is characterized in that said means to detect the state of the cell comprises a second transistor having a source, a drain and a gate, said second transistor being complementary to said first transistor and said gate of said second transistor being connected to said floating gate.